

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	46243	(scan\$4 or read\$3 or captur\$3 or extract\$6 or camera\$3 or detect\$6)same(segment\$6 or divid\$3 or partition\$3 or partial\$3 or portion\$3 or region\$3 or block\$3)same(compar\$6 or correlat\$4 or associat\$4 or match\$3 or similar\$4)same(filter\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 07:56
S2	9799	S1 same(correct\$4 or adjust\$4 or enhanc\$6 or smooth\$3)	US-PGPUB; USPAT	OR	ON	2007/06/01 07:57
S3	2576	S2 same(buffer\$4 or stor\$4 or memor\$4 or record\$3 or registor\$3 or databas\$3 or librar\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 07:57
S4	119	S3 same(train\$6 or untrain\$3 or learn\$4 or neural\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 07:58
S5	60	S4 same(frequenc\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 07:56
S6	8596	(imag\$3) same(scan\$4 or read\$3 or captur\$3 or extract\$6 or camera\$3 or detect\$6)same(segment\$6 or divid\$3 or partition\$3 or partial\$3 or portion\$3 or region\$3 or block\$3)same(compar\$6 or correlat\$4 or associat\$4 or match\$3 or similar\$4)same(filter\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:18
S7	2180	S6 same(correct\$4 or adjust\$4 or enhanc\$6 or smooth\$3)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:20
S8	674	S7 same(buffer\$4 or stor\$4 or memor\$4 or record\$3 or registor\$3 or databas\$3 or librar\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:20
S9	34	S8 same(train\$6 or untrain\$3 or learn\$4 or neural\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:21
S10	9504	(pictur\$3 or photograph\$3 or document\$3 or imag\$3) same(scan\$4 or read\$3 or captur\$3 or extract\$6 or camera\$3 or detect\$6)same(segment\$6 or divid\$3 or partition\$3 or partial\$3 or portion\$3 or region\$3 or block\$3)same(compar\$6 or correlat\$4 or associat\$4 or match\$3 or similar\$4)same(filter\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:18
S11	2371	S10 same(correct\$4 or adjust\$4 or enhanc\$6 or smooth\$3)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:20
S12	749	S11 same(buffer\$4 or stor\$4 or memor\$4 or record\$3 or registor\$3 or databas\$3 or librar\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:20

EAST Search History

S13	38	S12 same(train\$6 or untrain\$3 or learn\$4 or neural\$4)	US-PGPUB; USPAT	OR	ON	2007/06/01 13:21
S14	1	"5586317".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:22
S15	1	"5584050".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:22
S16	1	"5583966".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:23
S17	1	"5771311".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:27
S18	1	"5546195".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:27
S19	1	"5185850".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:27
S20	1	"5516216".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:29
S21	1	"5329380".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:29
S22	1	"5270728".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:30
S23	1	"5258854".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:30
S24	1	"6169552".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:31
S25	1	"5771311".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:35
S26	1	"5546195".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:35
S27	1	"5771311".PN.	USPAT; USOCR	OR	ON	2007/06/01 13:35
S28	1	"6542925".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:23
S29	1	"6526581".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:23
S30	1	"6526041".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:24
S31	1	"6249817".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:24
S32	1	"6111883".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:25
S33	1	"6018738".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:25
S34	1	"6514202".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:26

EAST Search History

S35	1	"6730035".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:26
S36	1	"6450964".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:27
S37	1	"6301498".PN.	USPAT; USOCR	OR	ON	2007/06/05 10:27
S38	1	"6450964".PN.	USPAT; USOCR	OR	ON	2007/06/05 11:45
S39	1	(fram\$4 and rat\$4 and temporar\$4 and stor\$4 and decimat\$4 and ratio\$4 and equal\$4 and less\$4 and larger\$4). clm.	US-PGPUB; USPAT	OR	ON	2007/06/05 11:47



[□ Search Result - Print Format](#)

[< Back](#)

Key: IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, II CNF = IEE Conference, IEEE STD = IEEE Standard

1. **A variable access associative memory chip for flexible searches in RAM**
Mayer, R.P.;
Computers and Communications, 1995. Conference Proceedings of the 1995 IEEE Fourteenth Annual International Phoenix Conference on
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2. **Efficient similarity search in streaming time sequences**
Kontaki, M.; Papadopoulos, A.N.;
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3. **Inference techniques for fault tolerant distributed database systems**
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Databases, Parallel Architectures and Their Applications, PARBASE-90, International Conference on
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4. **Address-free memory access based on program syntax correlation of loads and stores**
Lu Peng; Jih-Kwon Peir; Qianrong Ma; Lai, K.;
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5. **Demonstration of a parallel-access holographic RAM using the super-parallel holographic optical correlator architecture**
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Lasers and Electro-Optics, 2004. (CLEO). Conference on
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6. **Image organization and retrieval using a flexible shape model**
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7. **Access Structures for Angular Similarity Queries**
Tan Apaydin; Ferhatosmanoglu, H.;
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Volume 18, Issue 11, Nov. 2006 Page(s):1512 - 1525
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8. **An Efficient Pre-Traceback Architecture for the Viterbi Decoder Targeting Wireless Communication Applications**
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9. **On the File Design Problem for Partial Match Retrieval**
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10. **MgO-Based Tunnel Junction Material for High-Speed Toggle Magnetic Random Access Memory**
Dave, R.W.; Steiner, G.; Slaughter, J.M.; Sun, J.J.; Craigo, B.; Pietambaram, S.; Smith, K.; Grynkeiwich, G.; DeHerrera, M.; Akerman, J.; Tehrani, S.;
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11. **A 100-mega-access per second matching memory for a data-driven microprocessor**
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Solid-State Circuits, IEEE Journal of
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12. **Comparative modeling and evaluation of CC-NUMA and COMA on hierarchical ring architectures**
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Volume 6, Issue 12, Dec. 1995 Page(s):1316 - 1331
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13. **A strategy to manage cache consistency in a disconnected distributed environment**
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Volume 12, Issue 7, July 2001 Page(s):686 - 700
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14. **A design for high-speed low-power CMOS fully parallel content-addressable memory macros**
Miyatake, H.; Tanaka, M.; Mori, Y.;
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15. **Fast indexing and visualization of metric data sets using slim-trees**
Traina, C., Jr.; Traina, A.; Faloutsos, C.; Seeger, B.;
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16. **A switched-current sensing architecture for a four-state per cell magnetic tunnel junction MRAM**
Au, E.K.S.; Wing-Hung Ki; Wai Ho Mow; Hung, S.T.; Wong, C.Y.;
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17. **Architectural Leakage-Aware Management of Partitioned Scratchpad Memories**
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18. High Performance Block I/O for Global File System (GFS) with InfiniBand RDMA

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20. Architecture of a stereo matching VLSI processor based on hierarchically parallel memory access

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24. Comparison of replication of the user mobility profile with caching for reduction of HLR accesses

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25. Implementation design for a kernelized trusted DBMS

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